



Pentium® III Processor Identification and Package Information

S-Spec	Core Steppings	CPUID	Speed (MHz) Core/Bus ¹¹	L2 Size (Kbytes)	Tag RAM/ Steppings	ECC/ Non-ECC	Processor Substrate Revision	Package and Revision	Notes
SL364	kB0	0672h	450/100	512	T6P-e/A0	ECC	D	SECC2 [†]	1, 2, 4
SL365	kB0	0672h	500/100	512	T6P-e/A0	ECC	D	SECC2 [†]	1, 2, 4, 8
SL3CC	kB0	0672h	450/100	512	T6P-e/A0	ECC	D	SECC2 [†]	1, 2, 3, 4
SL3CD	kB0	0672h	500/100	512	T6P-e/A0	ECC	D	SECC2 [†]	1, 2, 3, 4
SL38E	kB0	0672h	450/100	512	T6P-e/A0	ECC	D	S.E.C.C	1, 2, 4
SL38F	kB0	0672h	500/100	512	T6P-e/A0	ECC	D	S.E.C.C	1, 2, 4
SL35D	kC0	0673h	450/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4
SL37C	kC0	0673h	450/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4
SL35E	kC0	0673h	500/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4
SL37D	kC0	0673h	500/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4
SL3F7	kC0	0673h	550/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4
SL3FJ	kC0	0673h	550/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4
SL3BN	kC0	0673h	533B/133	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4, 10
SL3E9	kC0	0673h	533B/133	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4, 10
SL3JM	kC0	0673h	600/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4
SL3JT	kC0	0673h	600/100	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4
SL3JP	kC0	0673h	600B/133	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 4, 10
SL3JU	kC0	0673h	600B/133	512	T6P-e/A0	ECC	E	SECC2 [†]	1, 3, 4, 10
SL3Q9	cA2	0681h	500E/100	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3R2	cA2	0681h	500E/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL3VF	cA2	0681h	533EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3VA	cA2	0681h	533EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL3QA	cA2	0681h	550E/100	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3R3	cA2	0681h	550E/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL3VH	cA2	0681h	600E/100	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3NL	cA2	0681h	600E/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10



PENTIUM® III PROCESSOR SPECIFICATION UPDATE

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SLVG	cA2	0681h	600EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3VB	cA2	0681h	600EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL3VJ	cA2	0681h	650/100	256	N/A	ECC	B	FC-PGA (370 pin)	9
SL3NM	cA2	0681h	650/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9
SL3VK	cA2	0681h	667/133	256	N/A	ECC	B	FC-PGA (370 pin)	9
SL3T2	cA2	0681h	667/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9
SL3VL	cA2	0681h	700/100	256	N/A	ECC	B	FC-PGA (370 pin)	9
SL3T3	cA2	0681h	700/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9
SL3VM	cA2	0681h	733/133	256	N/A	ECC	B	FC-PGA (370 pin)	9
SL3T4	cA2	0681h	733/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9
SL3VN	cA2	0681h	750/100	256	N/A	ECC	B	FC-PGA (370 pin)	9
SL3VC	cA2	0681h	750/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9
SL3WB	cA2	0681h	800EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3VE	cA2	0681h	800EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL3X4	cA2	0681h	800/100	256	N/A	ECC	B	FC-PGA (370 pin)	9, 10
SL3VD	cA2	0681h	800/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 9, 10
SL444/S L446	cB0	0683h	500E/100	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL45R	cB0	0683h	500E/100	256	N/A	ECC	B	FC-PGA (370 pin)	10, 7
SL3XS	cB0	0683h	533EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL45S	cB0	0683h	533EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	10, 7
SL44G	cB0	0683h	550E/100	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL45T	cB0	0683h	550E/100	256	N/A	ECC	B	FC-PGA (370 pin)	10, 7



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S-Spec	Core Steppings	CPUID	Speed (MHz) Core/Bus ¹¹	L2 Size (Kbytes)	Tag RAM/Steppings	ECC/Non-ECC	Processor Substrate Revision	Package and Revision	Notes
SL3XT	cB0	0683h	600EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL45V	cB0	0683h	600EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	10, 7
SL3XU	cB0	0683h	600E/100	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL45U	cB0	0683h	600E/100	256	N/A	ECC	B	FC-PGA (370 pin)	10, 7
SL3XV	cB0	0683h	650/100	256	N/A	ECC	B	FC-PGA (370 pin)	
SL45W	cB0	0683h	650/100	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL3XW	cB0	0683h	667/133	256	N/A	ECC	B	FC-PGA (370 pin)	
SL45X	cB0	0683h	667/133	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL3XX	cB0	0683h	700/100	256	N/A	ECC	B	FC-PGA (370 pin)	
SL45Y	cB0	0683h	700/100	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL45Z	cB0	0683h	733/133	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL3XY	cB0	0683h	733/133	256	N/A	ECC	B	FC-PGA (370 pin)	
SL3XZ	cB0	0683h	750/100	256	N/A	ECC	B	FC-PGA (370 pin)	
SL462	cB0	0683h	750/100	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL3Y2	cB0	0683h	800EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL464	cB0	0683h	800EB/133	256	N/A	ECC	B	FC-PGA (370 pin)	7, 10
SL3Y3	cB0	0683h	800/100	256	N/A	ECC	B	FC-PGA (370 pin)	10
SL463	cB0	0683h	800/100	256	N/A	ECC	B	FC-PGA (370 pin)	7, 10
SL43H	cB0	0683h	850/100	256	N/A	ECC	B	FC-PGA (370 pin)	
SL49G	cB0	0683h	850/100	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL43J	cB0	0683h	866/133	256	N/A	ECC	B	FC-PGA (370 pin)	
SL49H	cB0	0683h	866/133	256	N/A	ECC	B	FC-PGA (370 pin)	7



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SL44J	cB0	0683h	933/133	256	N/A	ECC	B	FC-PGA (370 pin)	
SL49J	cB0	0683h	933/133	256	N/A	ECC	B	FC-PGA (370 pin)	7
SL4CM	cC0	0686h	600E/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4CL	cC0	0686h	600EB/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4CK	cC0	0686h	650/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4CJ	cC0	0686h	667/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4CH	cC0	0686h	700/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4M7	cC0	0686h	700/100	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4CG	cC0	0686h	733/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4M8	cC0	0686h	733/133	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4CF	cC0	0686h	750/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4M9	cC0	0686h	750/100	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4CE	cC0	0686h	800/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4MA	cC0	0686h	800/100	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13



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S-Spec	Core Steppings	CPUID	Speed (MHz) Core/Bus ¹¹	L2 Size (Kbytes)	Tag RAM/Steppings	ECC/Non-ECC	Processor Substrate Revision	Package and Revision	Notes
SL4CD	cC0	0686h	800EB/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4MB	cC0	0686h	800EB/133	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4CC	cC0	0686h	850/100	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4MC	cC0	0686h	850/100	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4CB	cC0	0686h	866/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4MD	cC0	0686h	866/133	256	N/A	ECC	C	FC-PGA (370 pin)	7, 13
SL4C9	cC0	0686h	933/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4ME	cC0	0686h	933/133	256	N/A	ECC	C	FC-PGA (370 pin)	7, 15
SL4C8	cC0	0686h	1B GHz/133	256	N/A	ECC	C	FC-PGA (370 pin)	
SL4MF	cC0	0686h	1B GHz/133	256	N/A	ECC	C	FC-PGA (370 pin)	15
SL3H7	cA2	0681h	600EB/133	256	N/A	ECC	B	SECC2	10
SL3NB	cA2	0681h	600EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3KV	cA2	0681h	650/100	256	N/A	ECC	B	SECC2	
SL3NR	cA2	0681h	650/100	256	N/A	ECC	B	SECC2	8
SL3KW	cA2	0681h	667/133	256	N/A	ECC	B	SECC2	
SL3ND	cA2	0681h	667/133	256	N/A	ECC	B	SECC2	8
SL3S9	cA2	0681h	700/100	256	N/A	ECC	B	SECC2	
SL3SY	cA2	0681h	700/100	256	N/A	ECC	B	SECC2	8
SL3SB	cA2	0681h	733/133	256	N/A	ECC	B	SECC2	
SL3SZ	cA2	0681h	733/133	256	N/A	ECC	B	SECC2	8
SL3H6	cA2	0681h	600E/100	256	N/A	ECC	B	SECC2	10
SL3N6	cA2	0681h	533EB/133	256	N/A	ECC	B	SECC2	10
SL3SX	cA2	0681h	533EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3V5	cA2	0681h	550E/100	256	N/A	ECC	B	SECC2	8, 10



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SL3N7	cA2	0681h	550E/100	256	N/A	ECC	B	SECC2	10
SL3NA	cA2	0681h	600E/100	256	N/A	ECC	B	SECC2	8, 10
SL3WC	cA2	0681h	750/100	256	N/A	ECC	B	SECC2	
SL3V6	cA2	0681h	750/100	256	N/A	ECC	B	SECC2	8
SL3Z6	cA2	0681h	800/100	256	N/A	ECC	B	SECC2	
SL3V7	cA2	0681h	800/100	256	N/A	ECC	B	SECC2	8, 10
SL3WA	cA2	0681h	800EB/133	256	N/A	ECC	B	SECC2	10
SL3V8	cA2	0681h	800EB/133	256	N/A	ECC	B	SECC2	8, 10
SL4G7	cA2	0681h	800EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3XG	cB0	0683h	533EB/133	256	N/A	ECC	B	SECC2	10
SL44W	cB0	0683h	533EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3XH	cB0	0683h	550E/100	256	N/A	ECC	B	SECC2	10
SL44X	cB0	0683h	550E/100	256	N/A	ECC	B	SECC2	8, 10
SL43E	cB0	0683h	600E/100	256	N/A	ECC	B	SECC2	10
SL44Y	cB0	0683h	600E/100	256	N/A	ECC	B	SECC2	8, 10
SL3XJ	cB0	0683h	600EB/133	256	N/A	ECC	B	SECC2	10
SL44Z	cB0	0683h	600EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3XK	cB0	0683h	650/100	256	N/A	ECC	B	SECC2	10
SL452	cB0	0683h	650/100	256	N/A	ECC	B	SECC2	8, 10
SL3XL	cB0	0683h	667/133	256	N/A	ECC	B	SECC2	10
SL453	cB0	0683h	667/133	256	N/A	ECC	B	SECC2	8, 10
SL3XM	cB0	0683h	700/100	256	N/A	ECC	B	SECC2	
SL454	cB0	0683h	700/100	256	N/A	ECC	B	SECC2	8
SL3XN	cB0	0683h	733/133	256	N/A	ECC	B	SECC2	
SL455	cB0	0683h	733/133	256	N/A	ECC	B	SECC2	8



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SL3XP	cB0	0683h	750/100	256	N/A	ECC	B	SECC2	
SL456	cB0	0683h	750/100	256	N/A	ECC	B	SECC2	8
SL3XQ	cB0	0683h	800EB/133	256	N/A	ECC	B	SECC2	10
SL458	cB0	0683h	800EB/133	256	N/A	ECC	B	SECC2	8, 10
SL3XR	cB0	0683h	800/100	256	N/A	ECC	B	SECC2	
SL457	cB0	0683h	800/100	256	N/A	ECC	B	SECC2	8, 10
SL43F	cB0	0683h	850/100	256	N/A	ECC	B	SECC2	
SL47M	cB0	0683h	850/100	256	N/A	ECC	B	SECC2	8
SL43G	cB0	0683h	866/133	256	N/A	ECC	B	SECC2	
SL47N	cB0	0683h	866/133	256	N/A	ECC	B	SECC2	8
SL448	cB0	0683h	933/133	256	N/A	ECC	B	SECC2	
SL47Q	cB0	0683h	933/133	256	N/A	ECC	B	SECC2	6
SL4FP	cB0	0683h	1B GHz/133	256	N/A	ECC	B	SECC2	8, 9, 10
SL48S	cB0	0683h	1B GHz /133	256	N/A	ECC	B	SECC2	9, 10



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SL4C7	cC0	0686h	600E/100	256	N/A	ECC	C	SECC2	
SL4C6	cC0	0686h	600EB/133	256	N/A	ECC	C	SECC2	
SL4C5	cC0	0686h	650/100	256	N/A	ECC	C	SECC2	
SL4C4	cC0	0686h	667/133	256	N/A	ECC	C	SECC2	
SL4C3	cC0	0686h	700/100	256	N/A	ECC	C	SECC2	
SL4C2	cC0	0686h	733/133	256	N/A	ECC	C	SECC2	
SL4KD	cC0	0686h	733/133	256	N/A	ECC	C	SECC2	6
SL4FQ	cC0	0686h	733/133	256	N/A	ECC	C	SECC2	6
SL4BZ	cC0	0686h	750/100	256	N/A	ECC	C	SECC2	
SL4BY	cC0	0686h	800/100	256	N/A	ECC	C	SECC2	
SL4KF	cC0	0686h	800/100	256	N/A	ECC	C	SECC2	6
SL4BX	cC0	0686h	800EB/133	256	N/A	ECC	C	SECC2	
SL4G7	cC0	0686h	800EB/133	256	N/A	ECC	C	SECC2	6
SL4KG	cC0	0686h	800EB/133	256	N/A	ECC	C	SECC2	6
SL4BW	cC0	0686h	850/100	256	N/A	ECC	C	SECC2	
SL4KH	cC0	0686h	850/100	256	N/A	ECC	C	SECC2	6
SL4BV	cC0	0686h	866/133	256	N/A	ECC	C	SECC2	
SL4KJ	cC0	0686h	866/133	256	N/A	ECC	C	SECC2	6
SL4BT	cC0	0686h	933/133	256	N/A	ECC	C	SECC2	15
SL4BR	cC0	0686h	1 GHz/100	256	N/A	ECC	C	SECC2	15
SL4BS	cC0	0686h	1B GHz/133	256	N/A	ECC	C	SECC2	15
SL4HH	cC0	0686h	1.13 GHz/133	256	N/A	ECC	C	SECC2	12

[†] Unless otherwise noted, all Pentium III processors in S.E.C.C.2 package have an OLGA package core.



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NOTES:

1. These parts will only operate at the specified core to bus frequency ratio at which they were manufactured and tested. It is not necessary to configure the core frequency ratios by using the A20M#, IGNEE#, LINT[1]/NMI and LINT[0]/INTR pins during RESET.
2. These processors will not shut down automatically upon assertion of THERMTRIP#.
3. This is a boxed processor with an attached heatsink.
4. Performance-monitoring event counters do not reflect MOVD and MOVQ stores to memory on these processors.
5. These parts will not assert THERMTRIP#, nor will they shut down in the event of an over-temperature condition (e.g., $T_j = -135^\circ\text{C}$).
6. Pin AJ3 is removed from these parts.
7. This is a boxed processor with an unattached fan heatsink.
8. This is a boxed processor with an attached fan heatsink.
9. These processors will not be validated in Dual Processor (DP) applications.
10. The "E" and "B" designators distinguish between Pentium® III processors with the same core frequency but different system bus frequencies and/or cache implementations.
 - B = 133 MHz System Bus
 - E = Processors with "Advanced Transfer Cache" (CPUID 068x and greater only if a frequency overlap exists)If, for a given core frequency, Pentium III processors are only available with one system bus frequency and one cache implementation, the above designators will not be used (e.g., not all processors with "Advanced Transfer Cache" will have the "E" designation).
11. Speeds will be marked as MHz up to but not including 1.0 GHz. Speeds 1.0 GHz and above will have the GHz marking.
12. $V_{cc} = 1.80\text{V}$, $T_j = 60^\circ\text{C}$ for this 1.13 GHz processor with CPUID 0686.
13. $T_j = 80^\circ\text{C}$, $V_{cc} = 1.70\text{V}$.
14. $V_{cc} = 1.65\text{V}$.
15. $V_{cc} = 1.70\text{V}$ for these cCX core steppings. $T_j = 70^\circ\text{C}$ for 1.0 GHz. $T_j = 75^\circ\text{C}$ for 933 MHz.