



BIOS Update Release Notes

PRODUCTS: D915PCY, D915PCM, D915PBL, D915PGN, D915PSY (Standard BIOS)

Build 0040

About This Release:

- September 26, 2006
- CY91510A.86A.0040.2006.0926.1010
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated processor support.
- Fixed the issue where supervisor/user passwords do not support non-alphanumeric keyboard characters.
- Added fix to be compliant with Intel OSB spec 3.51.
- Corrected wrong spelling of "aperature" to "aperture".
- Added fix to enable Pulse Width Stretching function for SMSC Heceta.

BIOS Version 0039

About This Release:

- September 14, 2005
- CY91510A.86A.0039.2005.0913.1842
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Fix the issue where 25X multiplier is not supported on PSC CPU.
- Improved S3 resume time by not double loading the CPU.
- Fixed issue where HT logo is not shown when system wakes from hibernation.
- Set PCIe "Compliance Test Pattern" to enable for manufacturing mode.
- Added SMBIOS 2.4 spec support for PCIe and memory information.
- Added a workaround for issue of losing Marvell* LAN when system is shutdown using power button override.
- Disabled the option "PXE boot to Lan" when option "On Board Lan" is set to disabled.
- Corrected badge placement when badge is displayed with BMP logo.
- Workaround destroy and recovery floppy issue.
- Fixed protected CMOS data being cleared in manufacturing mode.
- Added "System in Manufacturing Mode" string in post screen during manufacturing mode.
- Added fixed which ship iflash when EMM386 installed to avoid fwh corruption.
- Fixed issue where some PCI Express* cards were not detected during POST.
- Fixed WOL not functioning issue.
- Fixed issue where CD recovery failed on ICH6 RAID platforms.
- Fixed issue where recovery was failing from ATAPI devices (CD-ROM, LS-120).

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- Fixed issue where the ITK Supplemental SMBIOS memory structure (SMBIOS Type 0BBh) is not present on DDR1 systems.
- Corrected the value programmed for TWTR.
- Update PCIe x16 Link Recovery Algorithm with Squelch Adjustment#1 and Squelch Adjustment#2.
- Fixed an issue where Power Button override when going into S3 caused system to hang on resume.
- Fixed Enhanced Intel Speedstep® Technology (EIST) not able to step down when using 3.0GHz CPU
- Updated Portuguese translation.

BIOS Version 0038

About This Release:

- September 7, 2005
- CY91510A.86A.0038.2005.0907.0213
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Fixed an issue with possible flash corruption.

BIOS Version 0037

About This Release:

- September 5, 2005
- CY91510A.86A.0037.2005.0904.1925
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Update to latest microcode revisions.

BIOS Version 0036

About This Release:

- September 4, 2005
- CY91510A.86A.0036.2005.0904.1925
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Fixed incorrect Japanese and Portuguese translations.
- Fixed the issue where supervisor/user password do not support non-alphanumeric keyboard characters.

BIOS Version 0035

About This Release:

- May 3, 2005
- CY91510A.86A.0035.2005.0429.1900
- Core Baseline version 01.07.00

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- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Reduced duration the fan spins at full speed while booting.
- Updated Verb table for 3 jack and 5 jack Realtek* Audio and added programming of SSID into Codec.
- Fixed issues with 2X PCI memory and I/O space allocations.
- Added support for specific PCIx cards.
- Fixed event log report error when there is only a USB keyboard present.
- Fixed issue to set “English” as default language using MFG jumper.
- Increased system security by increasing length of supervisor and user passwords from 7 to 16 characters and by changing its storage location.

BIOS Version 0031

About This Release:

- January 28, 2005
- CY91510A.86A.0031.2005.0128.0818
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated Express BIOS Update (EBU) packager to 2.05.08.

BIOS Version 0030

About This Release:

- January 26, 2005
- CY91510A.86A.0028.2005.0126.1809
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated processor support.

BIOS Version 0028

About This Release:

- December 20, 2004
- CY91510A.86A.0028.2004.1220.1732
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated processor support.
- Added Enhanced Intel SpeedStep® Technology support.
- Updated to latest Intel® Application Accelerator ICH6R SATA RAID OPROM code. Production Version 4.0.2.6810.

BIOS Version 0027

About This Release:

- December 1, 2004
- CY91510A.86A.0027.2004.1201.1942
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated processor support.
- Added POST beep codes when video card is not detected.
- Fixed issue where the USB 2.0 Legacy Support BIOS SETUP option was not visible in ITK.
- Added control option to enable/disable clock spread spectrum.

BIOS Version 0026**About This Release:**

- November 17, 2004
- CY91510A.86A.0026.2004.1117.1544
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Added SMBIOS structure to support Intel Stable Image Platform Program (ISIPP).
- Fixed installation issue for Video Driver using PCix add in card.

BIOS Version 0025**About This Release:**

- November 11, 2004
- CY91510A.86A.0025.2004.1111.1524
- Core Baseline version 01.07.00
- UNDI 4.1.16
- Marvell* UNDI 3.03

New Fixes/Features:

- Updated Marvell** PXE code to version 3.03.
- Updated MFG Default for USB Boot to Disabled.
- Fixed issues with 2X PCI memory and I/O space allocations.
- Updated the high CPU temperature in the BIOS hardware monitoring.
- Updated Realtek* Audio verb table.
- Fixed possible corruption of the INT3 vector.
- Fixed issue found in Microsoft Windows XP* when only a USB keyboard is attached to the system.
- Updated PXE UNDI code to Intel(R) Boot Agent FE v4.1.16.

BIOS Version 0022**About This Release:**

- September 29, 2004
- CY91510A.86A.0022.2004.0929.1341
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

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New Fixes/Features:

- Fix for boot order being reordered when a USB drive is inserted and then removed while the Floppy Drive is set to Disabled. Fixed the issue with L2 cache size display for PSCR0 Processor.
- Fixed Audio issue after S3 resume.
- Corrected CPU badge displays.
- Fixed an issue of intermittent hang when using DDR1 400 2.6V DIMMs.
- Added ASF support for non-Intel LAN vendors.
- Added DDR2 533 MHz 3-3-3 1.9V Auto-Detect code.
- Changed the default splash image name to "Default BIOS POST Image".
- Fixed the issue with L2 cache size display for specific processors.
- Fixed extended POST times with specific card readers.
- Fixed PC Mark* hang when using 2GB memory configuration.
- Implemented ICH6 BIOS spec update 1.6 SATA programming change, section #1. Also implemented SATA SRST Errata BIOS workaround.

BIOS Version 0021

About This Release:

- September 1, 2004
- CY91510A.86A.0021.2004.0901.1411
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Enabled VPD cloning for use with Intel® Integrator Toolkit.
- Added a switch in BIOS Setup to disable all USB ports, the switch is only available in maintenance mode.
- Improved manufacturing mode by not requiring keyboard input when a 2x12 or 1x4 power connector is not plugged into the system.
- Fixed issue involving devices installed in PCIx Slots.
- Fixed S3/S4 transition issue.
- Fixed issue found when installing USB Card Reader.
- Added workaround for hang when running 3DMark03*.
- Fixed issue when executing Intel® Integrator Toolkit in VM86 mode.
- Fixed the Hardware Monitor chip programming when using specific processors.
- Fixed manufacturing POST cards display issue.
- Fixed S3 resume issue.
- Fixed rear chassis fan fluctuation after resuming from S3.
- Fixed issue with 1GB memory installed.
- Added BTX BMI support for specific devices.
- New BMI Sequence for Heceta6 E support.
- Fixed resume from S3 issue involving 1394 devices.

BIOS Version 0015

About This Release:

- July 21, 2004
- CY91510A.86A.0015.2004.0721.1617
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

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- Updated PXE code to version 2.05.
- Updated video add-in code.
- Fixed corruption seen in some CD boot menus.
- Fixed boot issue involving SATA drives when RAID is enabled.
- Added a warning message in POST if 2x12 or 1x4 power connector is not plugged into the system.
- Added Russian, Korean, and Simplified Chinese Support.

BIOS Version 0014

About This Release:

- June 24, 2004
- CY91510A.86A.0014.2004.0624.1715
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Fixed POST issue found when a LAN card is installed, but no video card is present.
- Fixed PXE boot issue involving certain PCI IDE cards.
- Changed the default LED state of PA3.0 from yellow to green.
- Updated NS SIO GPIO programming.
- Enabled the memory timings set by Intel® Desktop Control Center to be programmed into the BIOS.

BIOS Version 0012

About This Release:

- June 9, 2004
- CY91510A.86A.0012.2004.0609.1806
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Corrected audio issue found when resuming from S3.

BIOS Version 0011

About This Release:

- June 4, 2004
- CY91510A.86A.0011.2004.0604.1705
- Core Baseline version 01.07.00
- UNDI 4.1.10
- Marvell* UNDI 2.03

New Fixes/Features:

- Initial release.

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